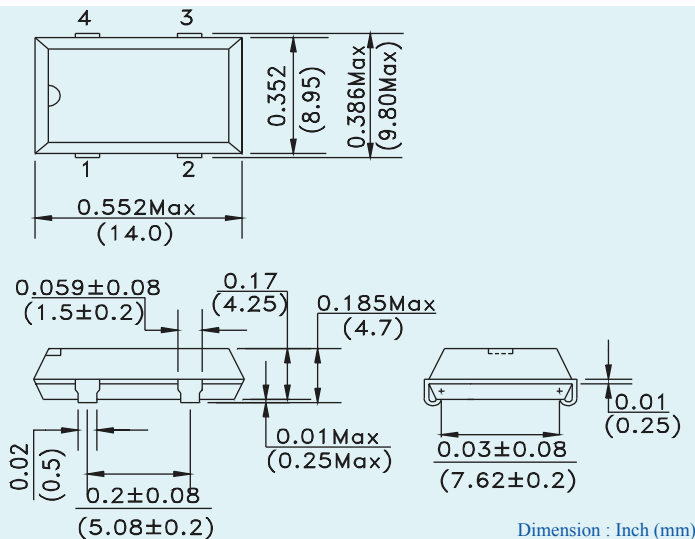


- PLASTIC OSC 14.0×9.8mm
 - Mode : Fundamental, 3rd
 - HCMOS / TTL OUTPUT
- Applications : Network equipment etc.

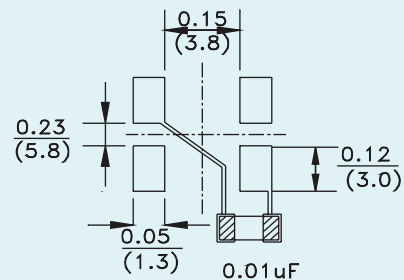
Standard Specification

| | | |
|-----------------------------------|---|---------------------------|
| Frequency Range | 1.8432MHZ to 48MHZ | |
| Supply voltage | 3.3V, 5.0V | |
| Current consumption | 45mA max | |
| Temperature Range | Operating | -40°C to +85°C, User spec |
| | Storage | -55°C to +125°C |
| Frequency Stability | ±50ppm max, User spec | |
| Output load condition(CMOS / TTL) | CMOS 15pF, 10TTL | |
| Symmetry (Duty) | 40% to 60%(STD), 45% to 55% (option) | |
| Output rise and fall time (tr/tf) | 7ns max (1.000MHZ ~ 30.000MHZ) 5ns max (30.001MHZ ~ 70.000MHZ) 4ns max (70.001MHZ ~ 125.000MHZ) | |
| High output voltage (COMS / TTL) | 90% Vdd, 2.4V min | |
| Low output voltage (CMOS / TTL) | 10% Vdd, 0.4V max | |
| Pin 1 Tri-Sate | Output enable voltage | No Connection |
| | Output enable voltage | 70% Vdd min |
| | Output disable voltage | 30% Vdd max |
| Oscillation start up time | 5ms max | |
| Aging | ± 5ppm max | |
| phase Jitter (12KHZ ~20MHZ) | 1 ps max | |

Dimensions (mm)



Recommended land pattern



| PIN | FUNCTION |
|-----|----------|
| 1 | Tristate |
| 2 | GND/Case |
| 3 | Output |
| 4 | Vdd |

Note: Recommend using an approximately 0.01uF bypass capacitor between pin 2 and 4.